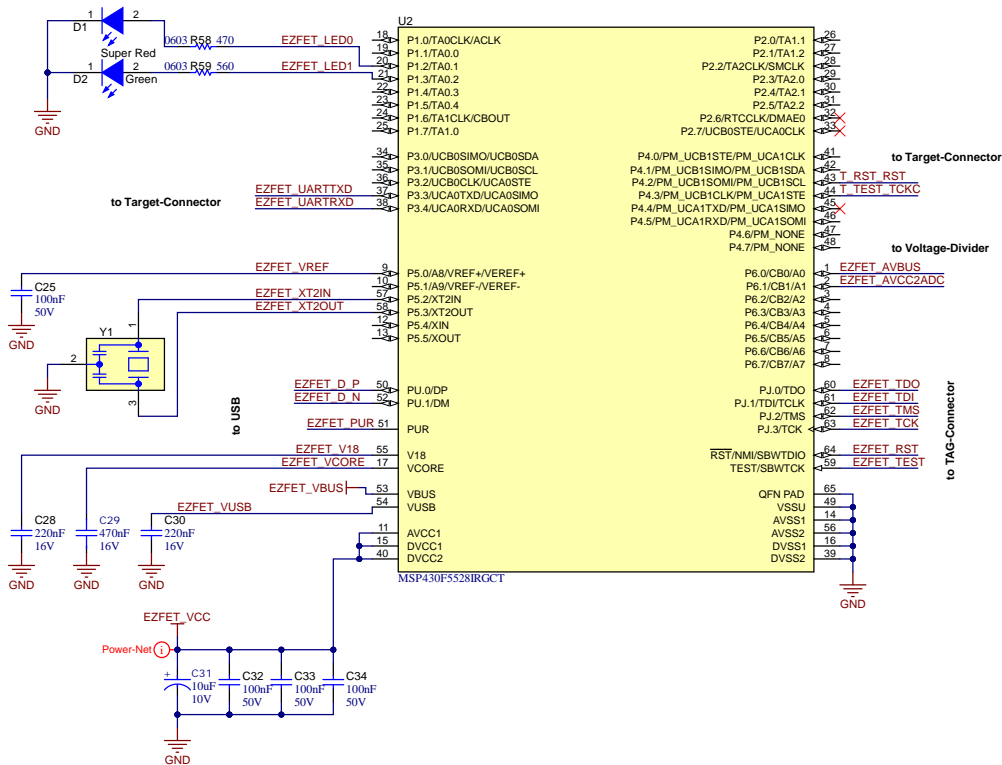
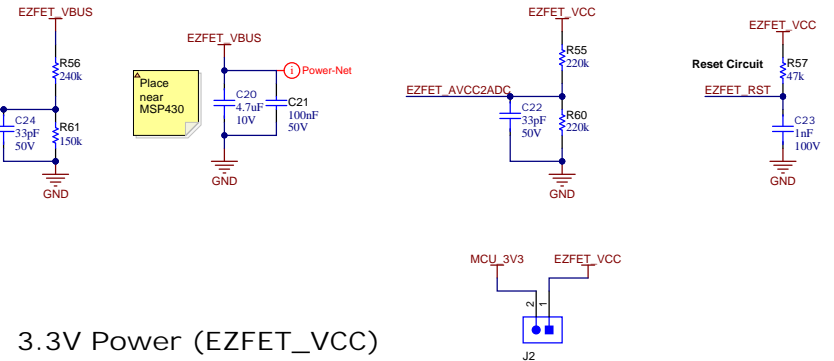
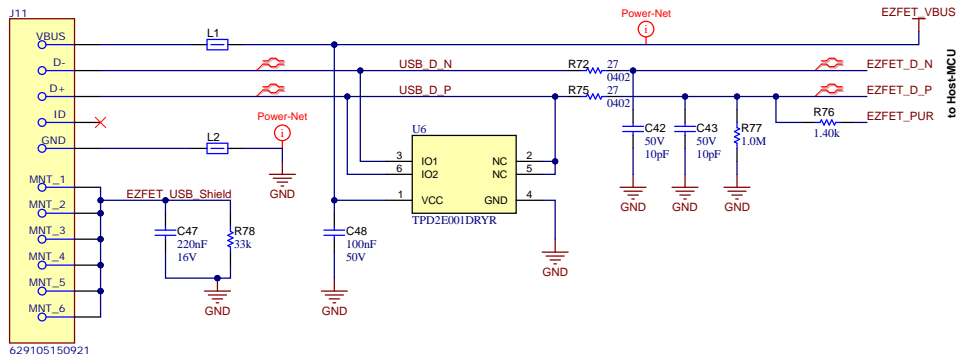


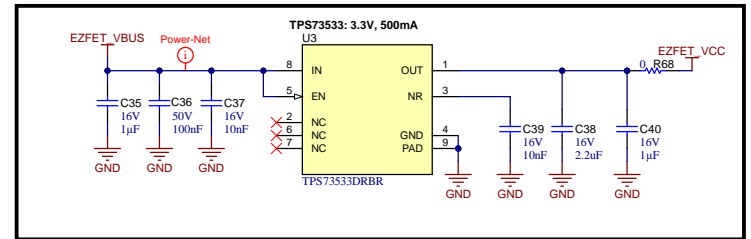
Host MCU for Emulation



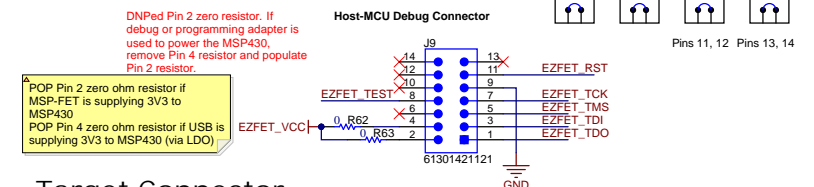
USB-I nterface



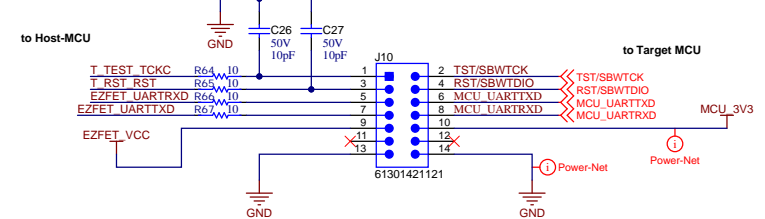
3.3V Power (EZFET_VCC)



JTAG-Connector (Host Debug)



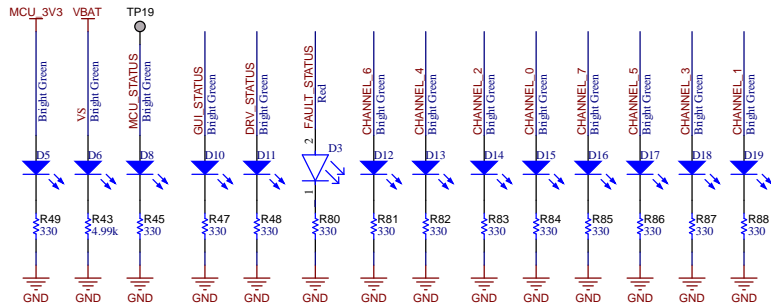
Target Connector



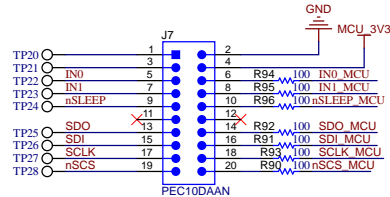
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: DRV8100x-Q1EVM	Designed for: Public Release	Mod. Date: 28-03-2024
TID #: N/A	Project Title: MD075	Sheet Title:
Number: MD-075	Rev: E1	Assembly Variant: 001
SVN Rev: Not in version control	File: MD081E1_ezFET_SchDoc	Sheet: 1 of 3
Drawn By: Murugavel Raju	Contact: http://www.ti.com/support	Size: B
Engineer: Audrey Kushler		http://www.ti.com

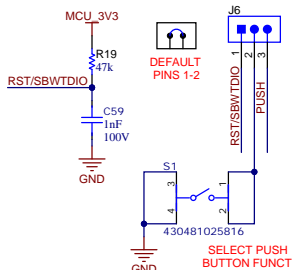
LEDS



Main Signal Header



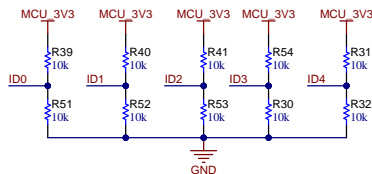
RST/PUSH Button



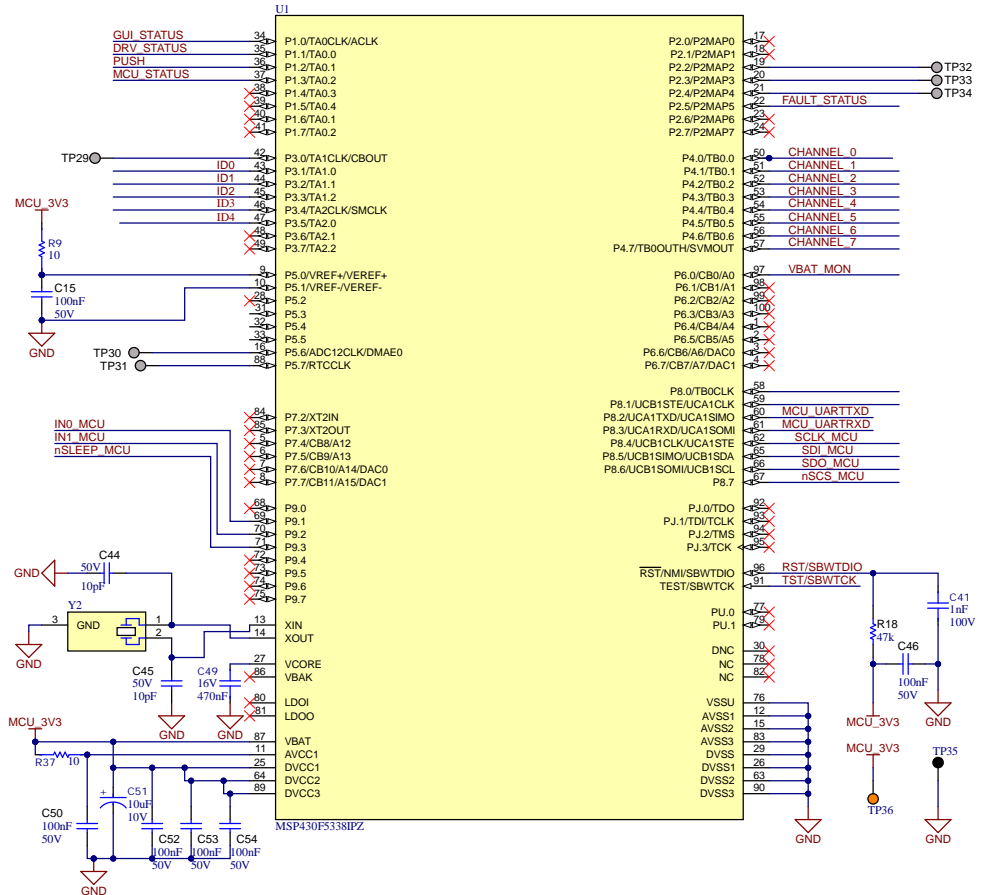
ID

The resistors on the ID[2:0] nets inform the firmware which device ID variant is on this board

Device	ID1	Type	ID3
DRV81008-Q1	0	Prerelease	0
DRV81004-Q1	1	Release	1
Just In Case	2		
Revision 1	3		
Revision 2	4		

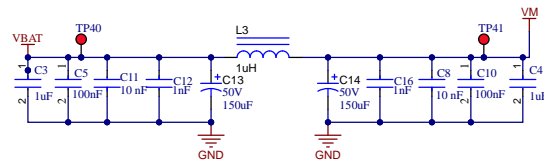
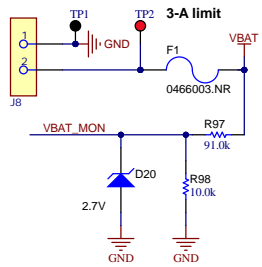


MSP430

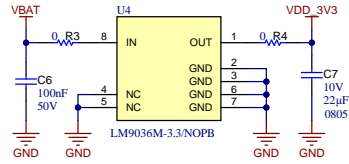


4.5 - 40V DC

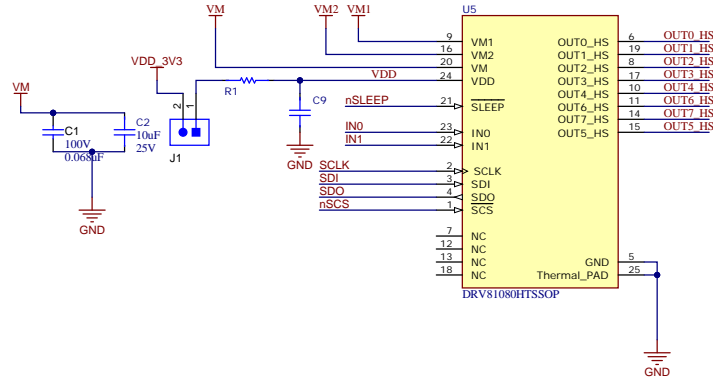
Board power



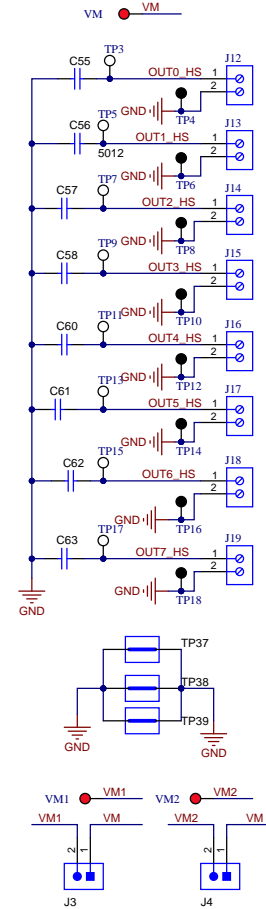
3.3V LDO



DRV81080-Q1




Output Connector



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Orderable: DRV8100x-Q1EVM	Designed for: Public Release	Mod. Date: 23-05-2024
TID #: N/A	Project Title: MD075	
Number: MD-075	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet 2 of 3
Drawn By: Tilden Chen	File: MD081E1 - DRV8100x SchDoc	Size: B
Engineer: Audrey Kuehler	Contact: http://www.ti.com/support	http://www.ti.com
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H1




SJ-5303 (CLEAR)

H2




SJ-5303 (CLEAR)

H3




SJ-5303 (CLEAR)


H4




SJ-5303 (CLEAR)



FID1




FID2



FID3

PCB LOGO

Texas Instruments




CE Mark

PCB LOGO


FCC disclaimer

PCB LOGO

WEEE logo



CAUTION HOT SURFACE



CAUTION HOT SURFACE

PCB LOGO

CAUTION. READ USER GUIDE BEFORE USE

LBL1

PCB Label

THT-14-423-10

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4


Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table

Variant	Label Text
001	DRV81008-Q1EVM

LBL2



EVM Folder

Title		
Size	Number	Revision
A		
Date:	6-08-2024	Sheet of
File:	D:\prakash\..\MD081E1_Hardware.SchDocDrawn By:	